

SPECIFICATIONS

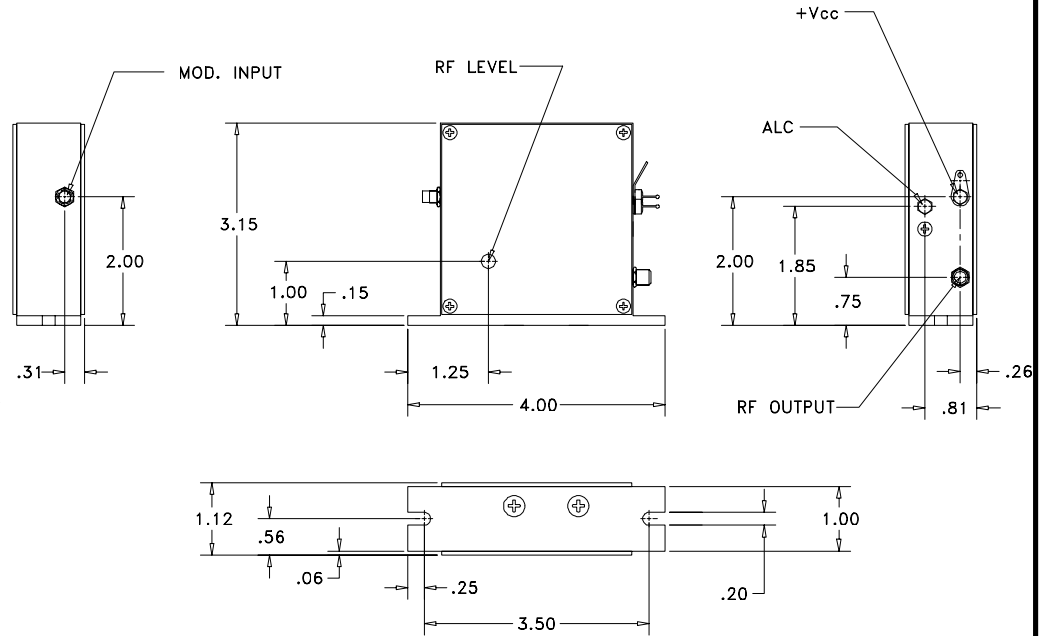
OUTLINE DRAWING

Modulation Input

Input Impedance 50 Ohms
 Analog Input (SMB Male) 0 to +1.0 VDC

RF Output

Center Frequency (Fc) 80 MHz \pm 0.1%
 Output Power (SMA Female) 2.0 W
 Rise/Fall Time 12 nsec Typ.
 RF Contrast Ratio 35 dB min
 Harmonic Distortion -30 dBc
 Output Impedance 50 Ohms
 Output VSWR 1.5 : 1 Max
 Power Supply Voltage (Filtered Feedthru) +28 V @ 600 mA
 ALC Voltage Level (Filtered Feedthru) +0 to +25 V nominal
 ALC Bandwidth 35 kHz
 RF Level Potentiometer Range 1.0 - 2.0 W



Notes:

1. The slope of the RF output power vs. the input signal voltage curve shall be non-zero and positive at all points between 0 and 1.0 Volts input, inclusive.
2. Output power factory set to 2 W at 1 Volt input. Power stability less than 5% over the heat sink's ambient temperature range of 0-40° C, after 5 minute warm-up.
3. When calculating the contrast ratio, it is understood that only the power of the 80 MHz fundamental shall be used. The higher harmonics have no effect on the AO modulator's performance.
4. A +25 Volt nominal input on the ALC corresponds to full RF output power. Zero RF power occurs at an ALC voltage slightly above 0 Volts. Full RF power occurs if ALC input is left unconnected.

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TOLERANCES: .XX \pm .01 .XXX \pm .005	DR	A. Campi 3/24/98	Crystal Technology, Inc.
MATERIAL:	CHK		DESCRIPTION: AODR 1080AF-AIF0-2.0
FINISH:	APP		PART NUMBER: 97-02207-35
	APP		REV: A
			SHEET 1 OF 1