



- NOTES:
- 7) C21 3pF bypass cap is on top of R3.
 - 6) All resistors are 1% 0805, all capacitors 5% 0805.
 - 5) Output 70MHz at 50mV into hi-Z Ohm load.
 - 4) 0V to +5V gives a change of ~600° (10.3rad) relative phase difference at outputs.
 - 3) Input 10MHz, 0.5V-4Vp-p, sine or square
 - 2) Adjust R11 for 0.75Vp-p ±10% at TP Phi-85° with Uphase = +2.5V.
 - 1) Adjust R6 for 0.75Vp-p ±10% at TP Phi-0° with Uphase = +2.5V.

B	Dec '03	Alan S.	IC1 pins, +2.5V/AGND, PCB spin, various.
Ltr	Date	By	Description
REVISIONS			
University of Toronto Quantum Optics Group			Dwg: RF Phase Shifter A. Steinberg's Lab
Drawn: Alan Stummer, Dec '03			Edited: 1/22/2004 01:10:15p
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