

REVISIONS

LTR	DESCRIPTION	DATE
-	ORIGINAL ISSUE PER EWR NO. V0002	03/24/95
A	CHANGE PER ECO NO. E0046	07/06/95
B	CHANGE PER ECO NO. E0121	01/31/96
C	CHANGE PER ECO NO. E0914	12/29/97
D	CHANGE PER ECO NO. E1899	07/10/00
E	CHANGE PER ECO NO. E2923	10/09/02
F	CHANGE PER ECO NO. E3612	01/26/04

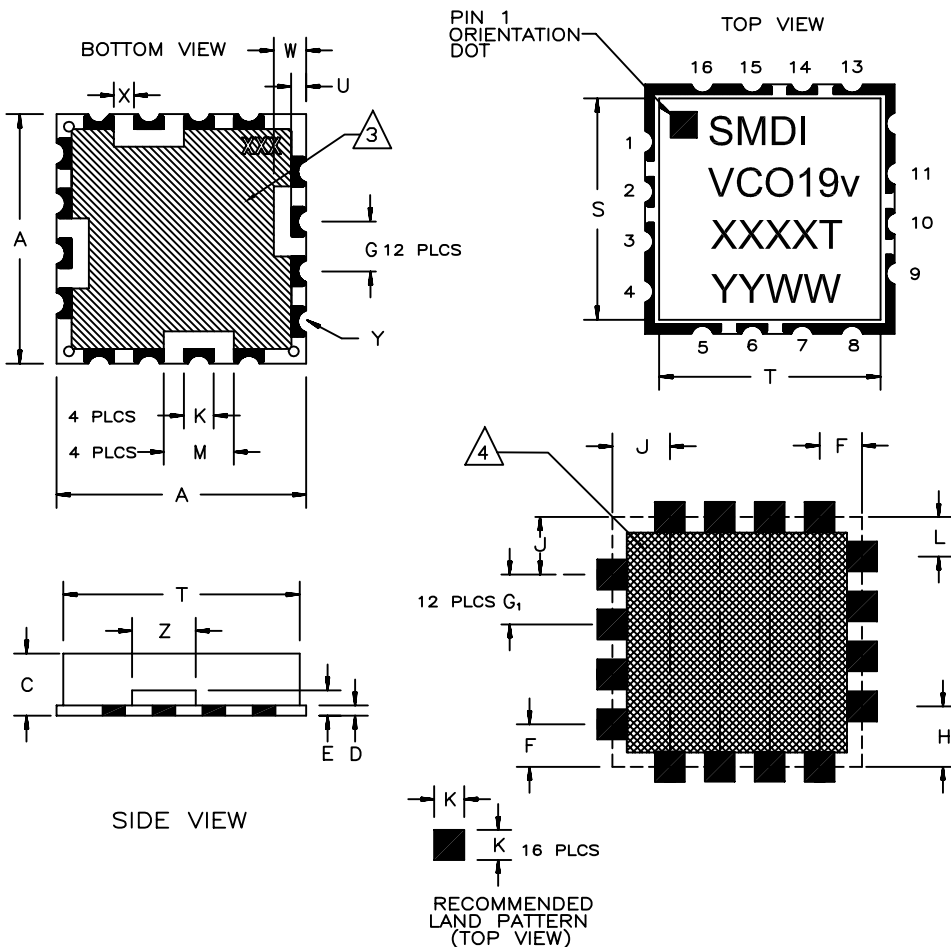
NOTE, UNLESS OTHERWISE SPECIFIED:

1. THE METAL CASE IS GROUND.
2. ALL HALF VIA CONTACTS ARE PLATED THRU FROM THE PAD ON THE TOP SIDE TO THE PAD ON THE BOTTOM SIDE OF THE BOARD.
3. HATCHED AREAS ARE GROUND AND ARE COVERED WITH LPI SOLDER MASK OVER BARE COPPER. ALL CONTACT AREAS ARE PLATED. SIGNAL VIAS MAY BE LOCATED WITHIN GROUND PLANE.
4. CROSS HATCHED AREA INDICATES AREA WHERE SOLDER MASK SHOULD BE APPLIED TO MOUNTING BOARD.
5. SUBSTRATE MATERIAL: FR-4.
6. XXXX REPRESENTS THE MODEL NUMBER.
7. YYWW IS THE DATE CODE.
8. v IS THE VOLTAGE DESIGNATOR.

PIN OUT FOR VCO	
PIN	APPLICATION
2	Vt
6	MODULATION (OPT)
10	RF OUT
14	VCC

ALL OTHER PINS ARE GROUND

SYMBOL	DIMENSION	
	MILLIMETERS	INCHES
A	12.70 ± 0.25	0.50 ± 0.010
C	SEE TABLE BELOW	
D	0.99 MAX	0.039 MAX
E	1.75 MAX	0.069 MAX
F	2.16	0.085
G	2.54 ± 0.08	0.100 ± 0.003
G <sub>1</sub>	2.54	0.100
H	3.07	0.121
J	2.92	0.115
K	1.52	0.060
L	2.00	0.079
M	3.56	0.140
S	11.68	0.460
T	11.81	0.465
U	0.76	0.030
W	1.65	0.065
X	1.02	0.040
Y	R = 0.58	R = 0.023
Z	3.25	0.128



DASH NO.	C DIMENSION	
	MILLIMETERS	INCHES
-1	5.66 ± 0.51	0.223 ± 0.020
-2	3.96 ± 0.51	0.156 ± 0.020
-3	2.54 ± 0.51	0.100 ± 0.020

APPROVALS

DATE

DRAWN BY	B. LANE	10/09/02
CHECKED BY		
PROCESS ENG		
DESIGN ENG		
QUALITY		
APPROVAL		



SIRENZA MICRODEVICES, INC.  
BROOMFIELD, COLORADO

TITLE

T-PACKAGE VCO  
MECHANICAL PCB OUTLINE

PROPRIETARY STATEMENT

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SIZE A	FSCM NO. 05375	DWG NO. 60035	REV. F
SCALE: NONE	DO NOT SCALE DWG	SHEET 1 OF 1	