



NOTES:
 Wire management: Richco SMWS1-1-01
 Microwave freq out is 96* f -reference.
ADF4007 PLL Eval Board Setup:
 LK1 set to 5U (CP)
 N1 & N2 set high (RF:64)
 M1 & M2 set high (CP, no Mux)
 Change R6 from 4.7K to 10K (CP Rset).
 Change R20 from 1.5K to 2.37K (Vref).
 Add 0.1uF/0603 across R20.
 C12 (C1 in ADIsim) = 1.8nF (0603/1608)
 R17 (R1 in ADIsim) = 150 (0603/1608)
 R19 (R2 in ADIsim) = 249 (0603/1608)
 C25 (C3 in ADIsim) = 10nF (0603/1608)
 C26 (C2 in ADIsim) = 1uF (0603/1608)
 R18 (R3 in ADIsim) = 0 Ohms (0603/1608)
 C27 (C4 in ADIsim) = out (0603/1608)
 Loop filter bandwidth = 2.8KHz with 47° phase margin,
 as calculated by Analog Device's ADIsimPLL program.

Ltr	Date	By	Description
REVISIONS			
University of Toronto Quantum Optics Group			Dual μ Wave EOM Driver DC supplies only A. Steinberg Labs
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