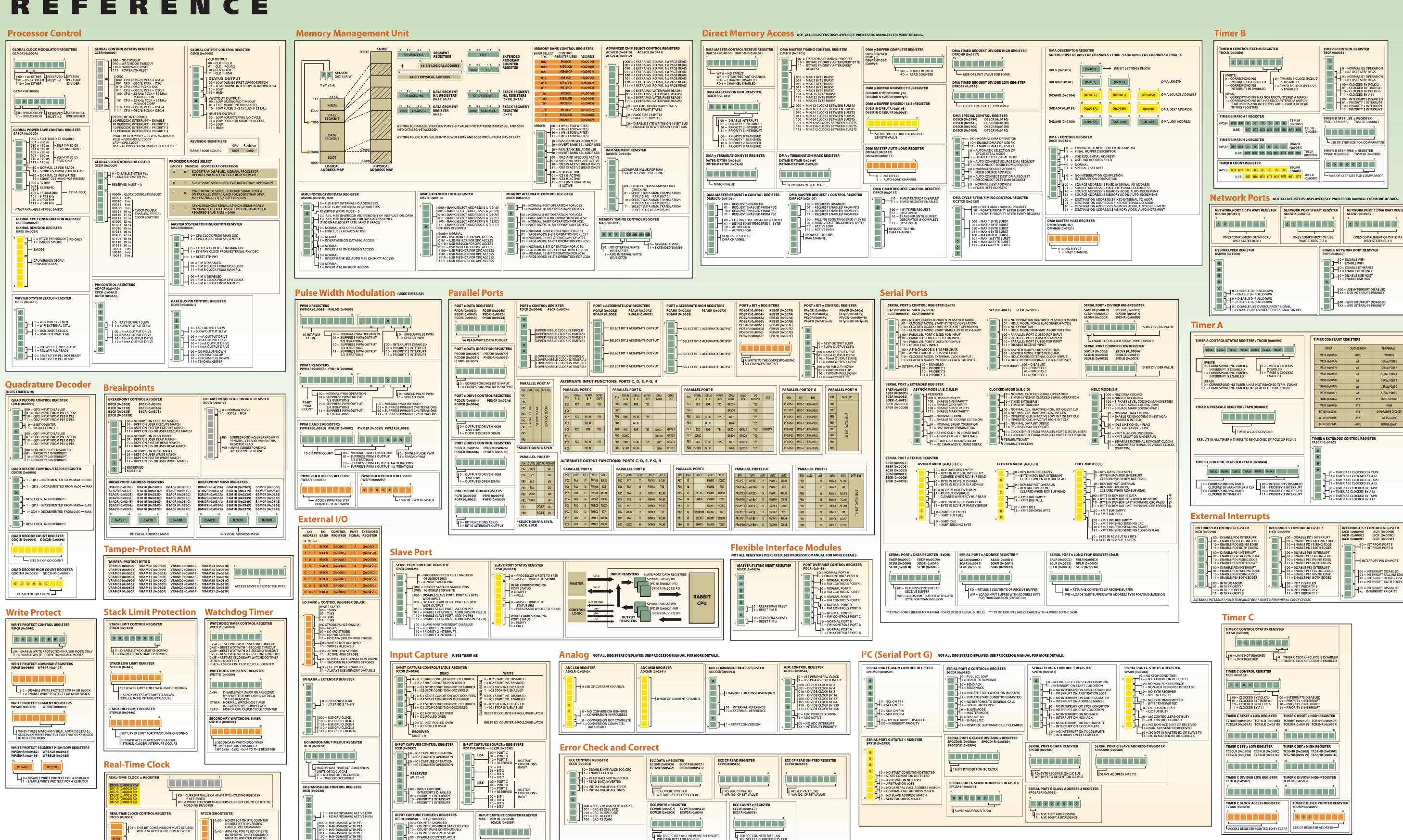
RABBIT 6000 MICROPROCESSOR

REGISTER LEGEND

REFERENCE





0x80 = RESET ALL SIX BYTES OF RTC TO 0x00. RESET MUST BE PRECEDED BY WRITING 0x40 TO ARM RESET FUNCTION

0xC0 = RESET ALL SIX BYTES IN RTC
COUNTER TO 0x00 AND REM
IN BYTE INCREMENT MODE

I/O HANDSHAKE SELECT REGISTER IHSR (0x0029)

00 = IGNORE START INPUT 01 = START AT START INPUT RISING EDGE 10 = START AT START INPUT FALLING EDGE 11 = START AT START INPUT EITHER EDGE

00 = IGNORE STOP INPUT 01 = STOP AT STOP INPUT RISING EDGE 10 = STOP AT STOP INPUT FALLING EDGE 11 = STOP AT STOP INPUT EITHER EDGE

READ LSB (LATCHES MSB)

D = NO EFFECT ON RTC COUNTER I = INCREMENT CORRESPONDING BYTE OF RTC (RTCxB)